

S850		USPT	5218723.pn.	<input type="checkbox"/>
S849		USPT	5218722.pn.	<input type="checkbox"/>
S848		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(enabl\$ with (write adj\$3 access) with (power adj\$2 reset) with (security)) and (@adL5 and (flag bit indicator)	<input type="checkbox"/>
S847		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(enabl\$ with (write adj\$3 access) with (power adj\$2 reset) with (security)) and (@ad<=2002 @py<=2002)	<input type="checkbox"/>
S846		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(enabl\$ with (write adj\$3 access) with (power adj\$2 reset) with (security)) AND @pd>20050808	<input type="checkbox"/>
S845		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enabl\$ with (write adj\$3 access) with (power adj\$2 reset) with (security)	<input type="checkbox"/>
S844		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enabl\$ with (write adj\$3 access) with (power adj\$2 reset)	<input type="checkbox"/>
S843		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enabl\$ with (write adj\$3 access)	<input type="checkbox"/>
S842		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enable with write with ("power-on reset") with (control\$ access\$ allow\$) and (@ad<=2002 @py<=2002)	<input type="checkbox"/>
S841		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enable with write with ("power-on reset") with (control\$ access\$ allow\$)	<input type="checkbox"/>
S840		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enable with write with ("power-on reset")	<input type="checkbox"/>
S839		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enable with write	<input type="checkbox"/>
S838		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	enable with (power adj on adj reset) with security	

S810		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(power adj reset) with (enabl\$) with (write\$) and (@adL4 and (secur\$)	<input type="checkbox"/>
S809		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(power adj reset) with (enabl\$) with (write\$) and (@ad<=2002 @py<=2002)	<input type="checkbox"/>
S808		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(power adj reset) with (enabl\$) with (write\$)	<input type="checkbox"/>
S807		PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(power adj reset) with (enabl\$)	